PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Mitsuru KITAMURA

Serial No.: 10/586,705 Group No.: 2872

Filed: July 20, 2006 Examiner: A. Amari

For: COMPUTER HOLOGRAM AND CREATION METHOD THEREOF

Attorney Docket No. U 016399-7

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF ENGLISH TRANSLATION OF JAPANESE OFFICE ACTION

Applicant refers to the paper filed on March 5, 2009 entitled "Submission of Japanese Office Action," during which Applicant submitted to the USPTO a copy of an Office Action mailed on February 17, 2009 from the Japanese Patent Office in connection with counterpart JP Application No. 2004-025009.

Applicant now attaches an English translation of that JPO Office Action for the Examiner's consideration.

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8a)

I hereby certify that this correspondence is, on the date shown below, being:

MAILING

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450

Date: March 17, 2009

FACSIMILE

☐ transmitted by facsimile to the Patent and Trademark Office to **(571) 273-8300**

EFS-WEB

☒ transmitted electronically

Signature

John Richards
(type or print name of person certifying)

It is again noted that the two references cited in that Office Action, JP2000-214750 (Dai Nippon) and JP2002-72838 (NTT), are already of record in the above-identified U.S. application.

Respectfully submitted,

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DECLARATION

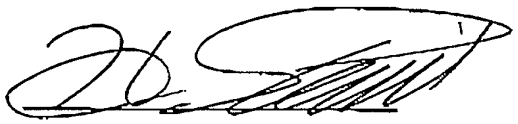
In the matter of the U.S. Patent Application No. 10/586,705

I, the undersigned, Hiroshi SHIMURA, whose full Post Office address is 6-1, Minamikamata 2-chome, Ohta-ku, Tokyo 144-0035 Japan do solemnly and sincerely declare as follows:

(1) That I am well acquainted with the English and Japanese languages and am competent to translate from Japanese into English.

(2) That I have executed, with the best of my ability, a true and correct translation into English of the Office Action of February 6, 2009 with respect to Japanese Patent Application No. 2004-025009, which corresponds to U.S. Patent Application No. 10/586,705, issued by Japanese Patent Office.

SIGNED AT: Tokyo Japan
DATE: March 12, 2009



Hiroshi SHIMURA

REASON OF REJECTION

Mailing No. 082013
Mailing date: February 17, 2009

Patent Application No. 2004-025009
Date: February 6, 2009
Examiner: Yasushi HORII, ID code 3713 2O00
Representative of applicant: Hiroshi SHIMURA
Applied provision: Article 29, Section 2

The present application is to be rejected under the following reasons. The applicant is requested to file an Argument stating his/her opinion in 60 days of the mailing date.

REASON

The inventions claimed in the following claims should not be patented under Article 29, Section 2 of Japanese Patent Law, because these inventions could easily have been made, prior to the filing of the patent application, by a person with ordinary skill in the art to which the invention pertains, on the basis of an invention or inventions which were described in a publication distributed in Japan or elsewhere prior to the filing of the patent application, or which were publicly utilized through electrical communication lines in Japan or elsewhere prior to the filing of the patent application.

(With respect to the citations, refer to LIST OF DOCUMENTS below)

For Claims 1-10:
References #1, #2:
Comment:

Reference #1 discloses "a method for creating a computer hologram", wherein a hologram is created by forming interference fringes on a predetermined recording surface by a computer-aided operation, said method comprises the steps of: defining a predetermined original image, a recording surface for recording the original image, and a reference light to be irradiated onto the recording surface; defining a large number of calculation points on the recording surface, and calculating, in terms of the individual calculation points, intensity of an interference wave formed by an object light emitted from a light source defined on the original image and the reference light; defining a plurality of types of binary patterns each defined by dividing a unit area having a fixed form and size into a first area having a first pixel value and a second area having a second pixel value by changing an occupancy ratio of the first area relative to the unit area; allocating, at positions of the respective calculation points, binary patterns having occupancy ratios corresponding to interference wave intensities in terms of the

respective calculation points, respectively; and creating physical fringes on a medium based on a binary image formed from an assembly of the binary patterns allocated onto the recording surface. (See the claims etc. of Reference #1)

Reference #2 discloses making a sampling pitch of a hologram less than a wavelength of reproduction light so as to avoid generating unnecessary reproduction light. (See the claims, paragraph [0005], etc. of Reference #2)

Therefore, it is easy for a person with ordinary skill in the art to make the inventions claimed in the claims of the present application by making a sampling pitch of a hologram, which is disclosed in Reference #1, less than a wavelength of reproduction light so as to avoid generating unnecessary reproduction light when it is reproduced.

LIST OF DOCUMENTS

Reference #1: JP2000-214750A
Reference #2: JP2002-72838A

RECORDS OF PRIOR ART DOCUMENT SEARCH

A technical field to have been examined: IPC G03H1/00-5/00

This record has nothing to do with the reason of rejection.

If you have any questions with respect to this Reason of Rejection, or if you want to arrange an interview with the examiner, contact to:

Yasushi, HORII, the Examiner, applied optics,
the first examining department of patent examining division, JPO.

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facsimile: 03-3501-0478